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Notice of Allowability	Application No.	Applicant(s)	
	10/827,276	LEE ET AL.	
	Examiner	Art Unit	
	Joseph L. Williams	2879	
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.			
1. This communication is responsive to			
2. The allowed claim(s) is/are <u>1,2,4-12 and 14-18</u> .			
3.			
attached Examiner's comment regarding REQUIREMENT	FOR THE DEPOSIT OF BIOLOGIC <i>i</i>	AL MATERIAL.	
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5. Notice of Informal P	atent Application (PT	O-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview Summary		
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	Paper No./Mail Dat 98), 7. ☐ Examiner's Amendn		
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ⊠ Examiner's Stateme	nt of Reasons for Allo	wance
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DETAILED ACTION

The amendment filed on 15 June 2006 has been entered and overcomes the 35 U.S.C. 112, second paragraph rejections of claims 3,13, and 14.

The amendment filed on 15 June 2006 has been entered and overcomes the objection to the specification.

Allowable Subject Matter

1. Claims 1, 2, 4-12, and 14-18 are allowed.

The following is an examiner's statement of reasons for allowance: Regarding independent claim 1, the prior art of record neither shows nor suggest a method for fabricating a mesh structure mounted between an anode plate and a cathode plate of a tetraode field-emission display, comprising; forming a soft insulation coating layer on a flat film; laminating a metal conductive plate as a converging electrode layer with a plurality of first apertures to the coating layer, such that a filler of the coating layer is filled in each first aperture; removing the coating layer but remaining the filler in each first aperture after baking, forming another coating layer on the converging electrode layer as an insulation layer, sintering to harden the insulation layer, forming a gate layer with a plurality of third apertures corresponding to the first apertures on the insulation layer, respectively, sintering to have the gate layer firmly attached on the insulation layer; forming one protective layer on the gate layer with a plurality of through holes corresponding to the third apertures, respectively, such that a plurality of second apertures formed on the insulation layer by etching, forming another protective layer on

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the converging electrode layer with another through hole corresponding to each first aperture, such that each filler is removed by etching; and removing the first and the second protective layers.

Due to their dependency, claims 2, 4-12 and 14-18 are necessarily allowable.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Joseph L. Williams whose telephone number is (571) 272-2465. The examiner can normally be reached on M-F (6:30 AM-3:00 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nimeshkumar D. Patel can be reached on (571) 272-2457. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Joseph L. Williams Primary Examiner Art Unit 2879